

Figure 1A

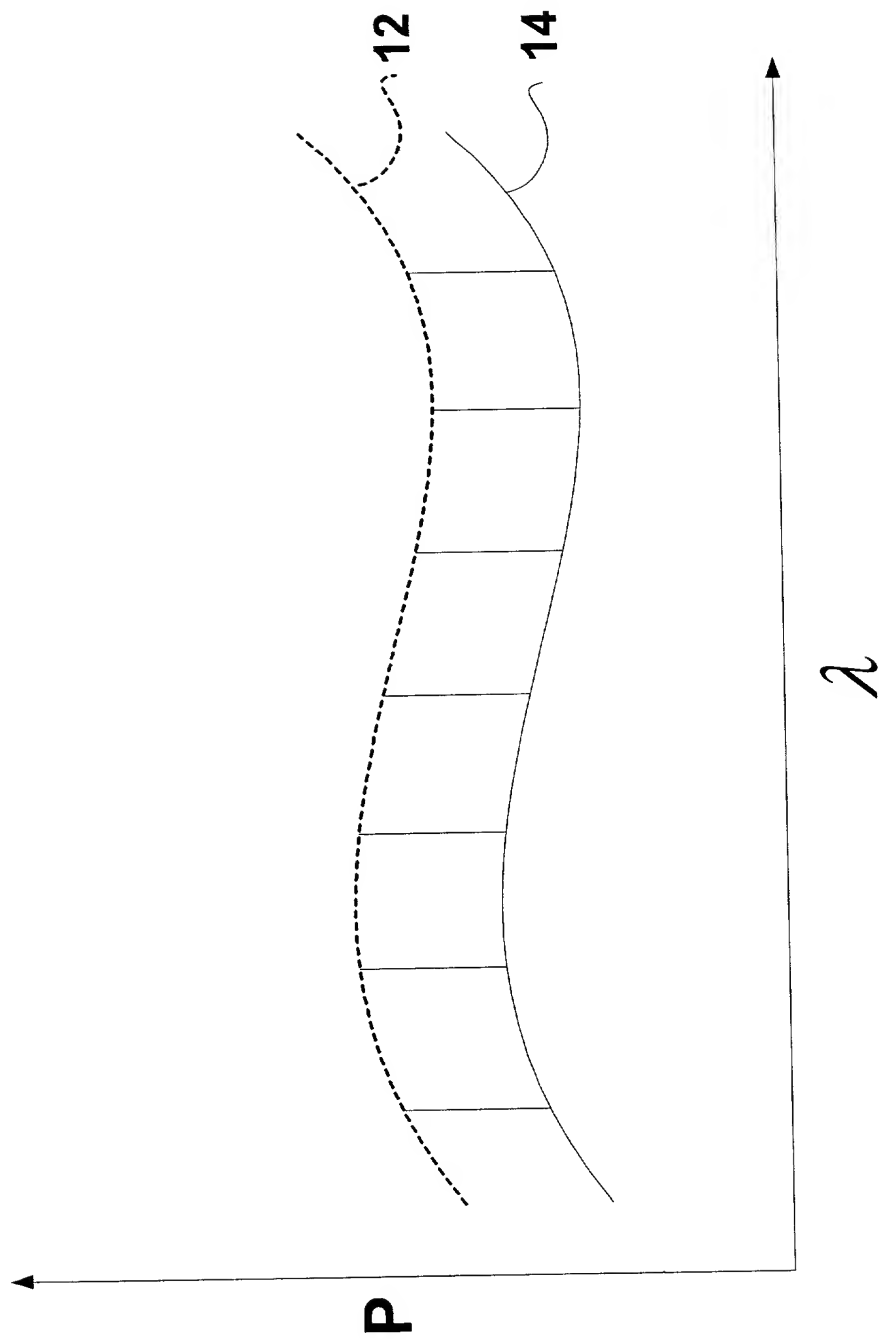
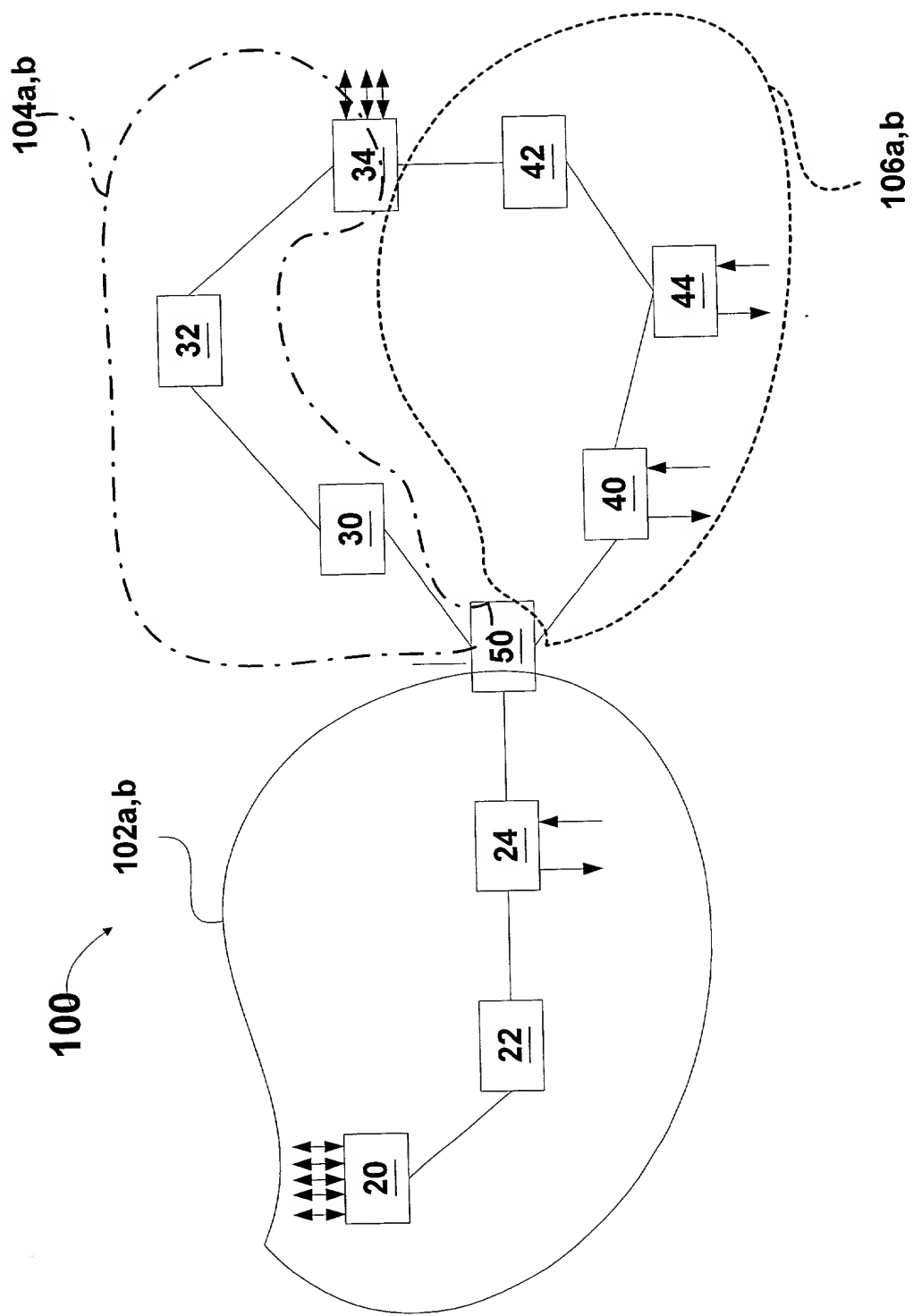


Figure 1B



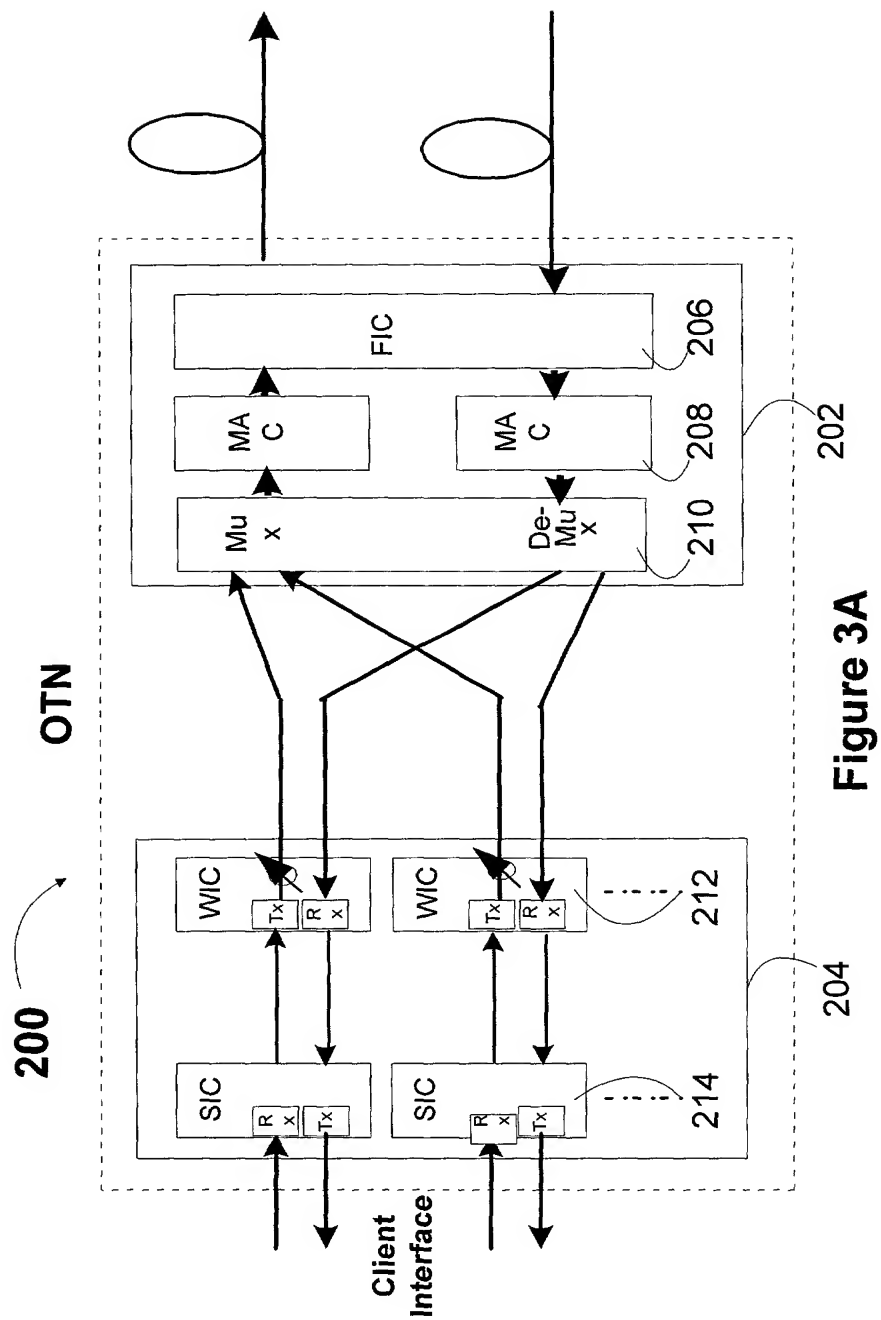
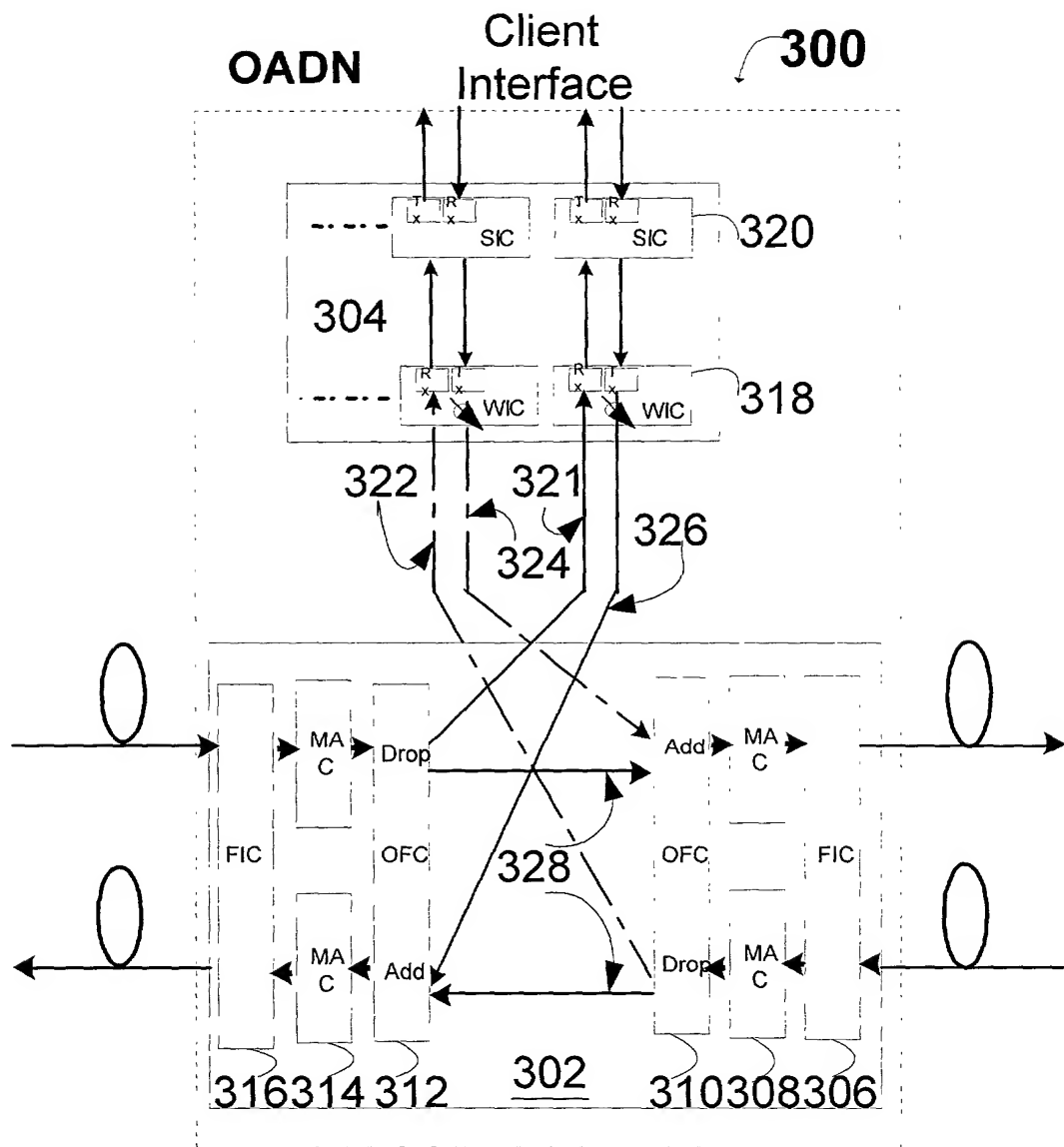


Figure 3A



**Figure 3B**

FIG. 4 is a block diagram of a system 400 for processing signals. The system 400 includes a series of processing blocks 402-1 through 402-6, each containing a CMC (Control Matrix Controller) block 404-1 through 404-4. The blocks are interconnected in a sequential manner, with feedback loops from the CMC blocks back to the processing blocks. The system 400 is configured to process signals at different wavelengths, as indicated by the labels  $\lambda_1$ ,  $\lambda_2$ , and  $\lambda_3$ .

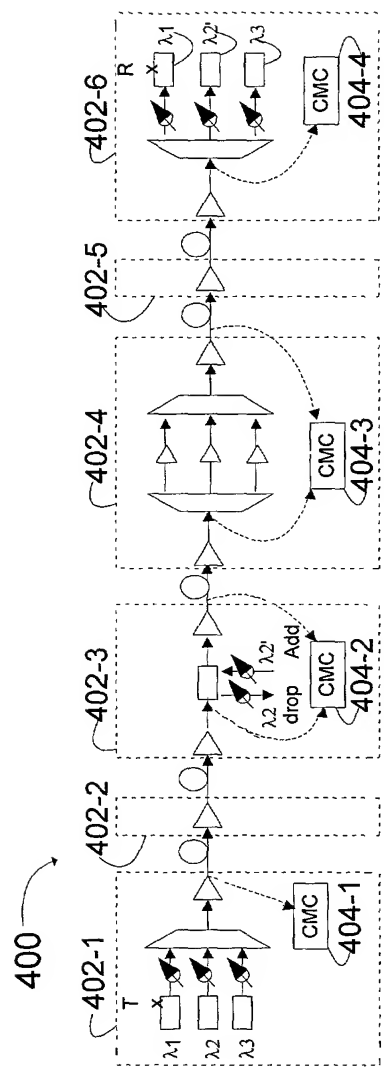


Figure 4

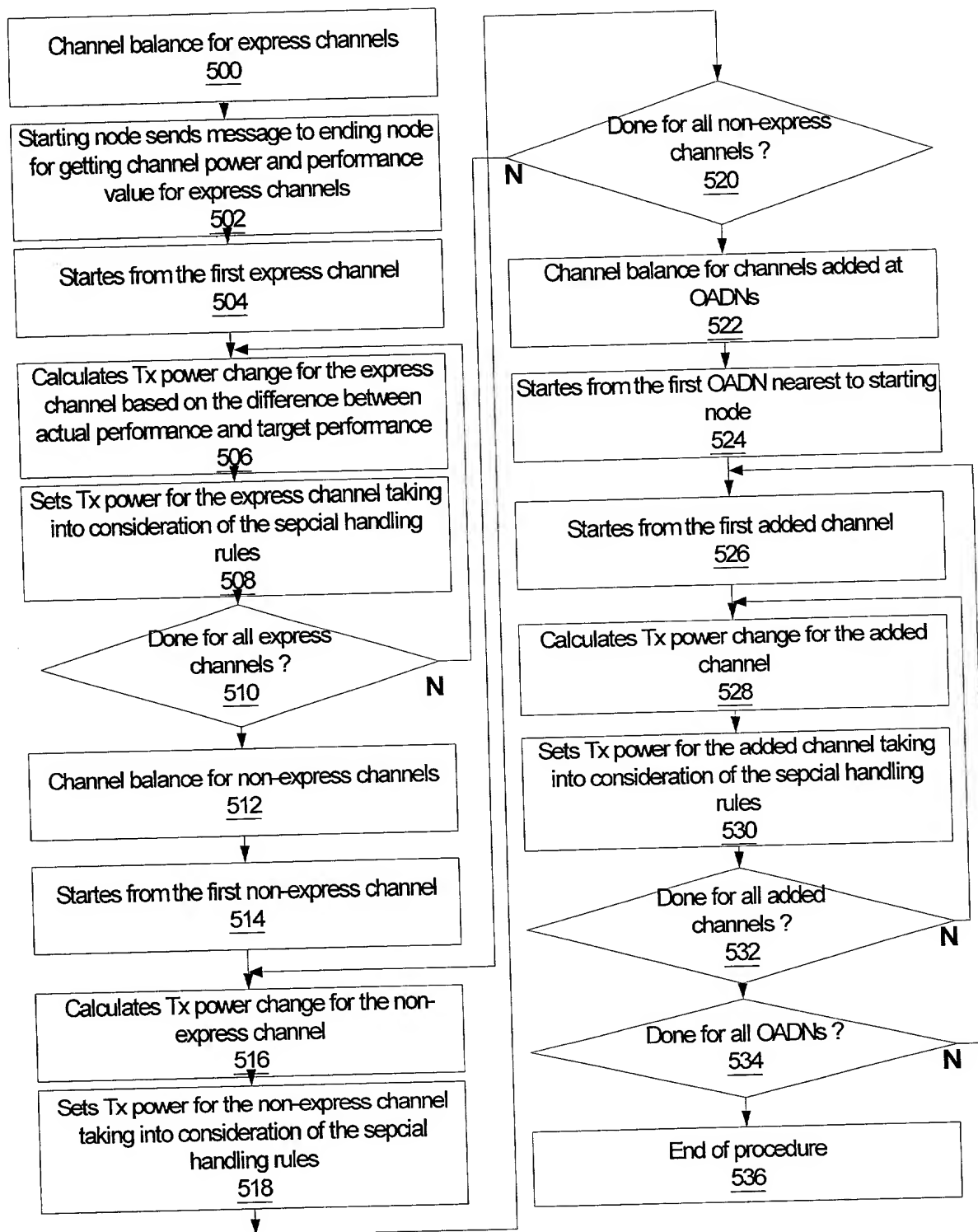


Figure 5